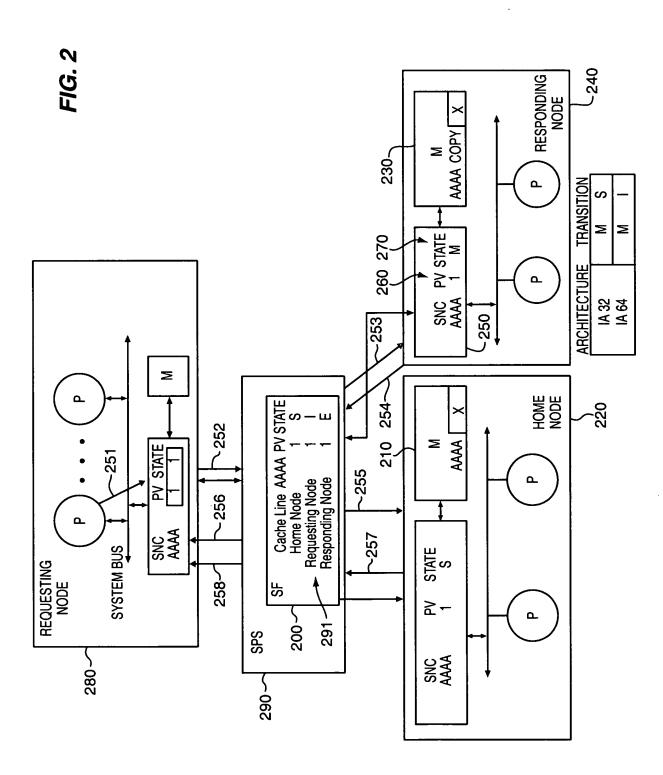


BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP nism for Efficiently Supporting the Full M e, Shared, Invalid) Protocol in a Cache Col Shared Memory System

Inventor: Manoj Khare, et al. Filing Date: December 29, 2000 Serial No.: 09/752,534

Sheet: 2 of 9



lti-Node

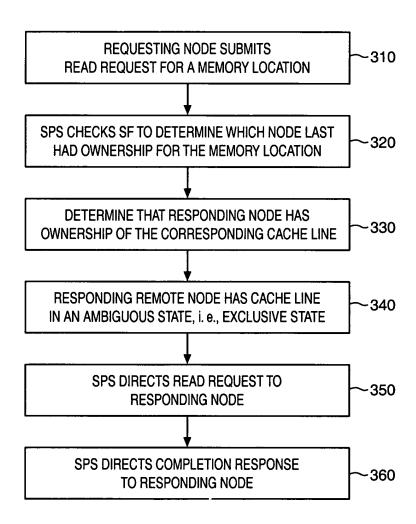


xclusive, Shared, Invalid) Protocol in a Carshared Memory System
Inventor: Manoj Khare, et al.

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FIG. 3



Confirm Read

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SNC

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REQUESTING NODE

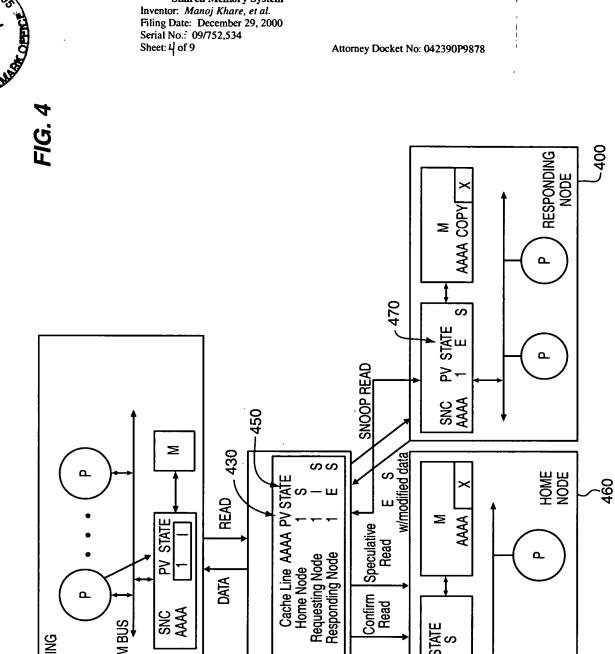
SYSTEM BUS

DATA

SF

SPS

440





A Mechanism for Efficiently Supporting to Exclusive, Shared, Invalid) Protocol in a **Shared Memory System** Inventor: Manoj Khare, et al.

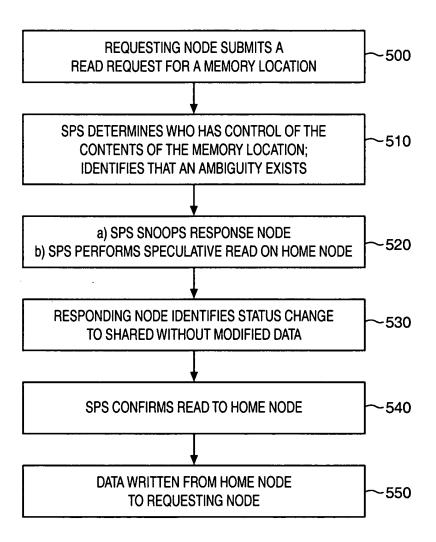
Filing Date: December 29, 2000 Serial No.: 09/752,534

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Attorney Docket No: 042390P9878

Iulti-Node

FIG. 5



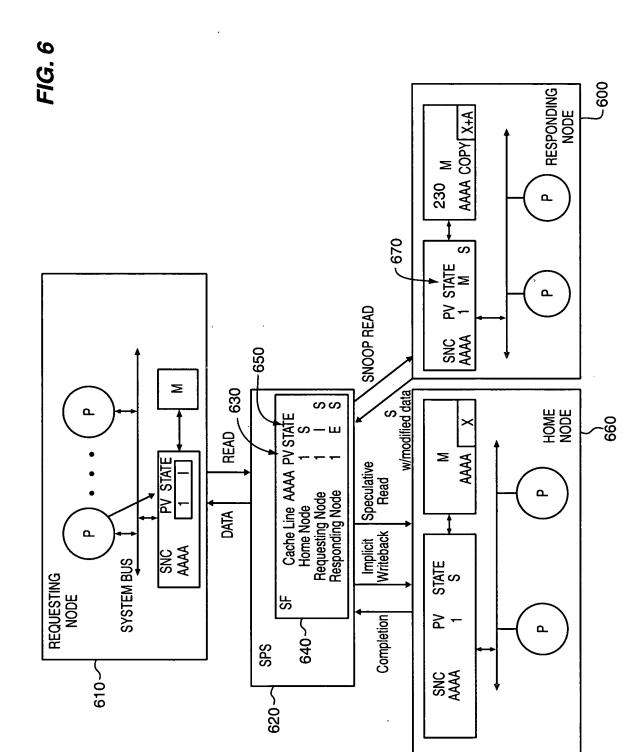


Exclusive, Shared, Invalid) Protocol in a Shared Memory System

Multi-Node

Inventor: Manoj Khare, et al. Filing Date: December 29, 2000 Serial No.: 09/752,534

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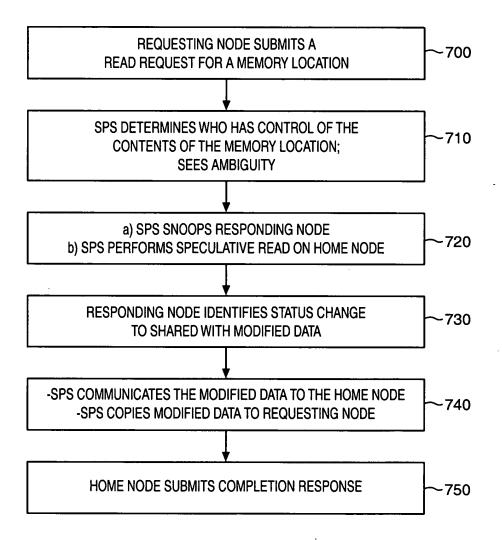
Iechanism for Efficiently Supporting the House ESI (Charles, Shared, Invalid) Protocol in a Cachardheren i-Nod Shared Memory System

Inventor: Manoj Khare, et al. Filing Date: December 29, 2000 Serial No.: 09/752,534

Sheet: 7 of 9

Attorney Docket No: 042390P9878

FIG. 7





Inventor: Manoj Khare, et al. Filing Date: December 29, 2000

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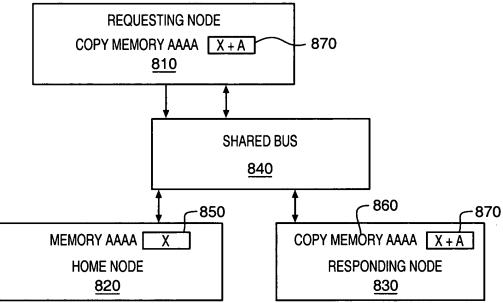


FIG. 8 PRIOR ART

Shared Memory System Inventor: Manoj Khare, et al. Filing Date: December 29, 2000 Serial No.: 09/752,534

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Attorney Docket No: 042390P9878

FIG. 9 PRIOR ART

